



Sri Venkatesa Perumal College of Engineering and Technology, Puttur

AUTONOMOUS-NAAC ACCREDITED
Puttur-517583, Andhra Pradesh

Department of Electronics & Communication Engineering

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OUR INSPIRATION



Dr. Ravuri Venkataswamy
Chairman



Mr. Ravuri Balaji
Vice-Chairman

ABOUT COLLEGE

Sri Venkatesa Perumal College of Engineering (SVPP), Puttur, Tirupati, Chittoor (dt.), A.P was established in 2001, and is Promoted by TAMILIAN EDUCATION ACADEMY.

Since its inception, it has transformed itself into a truly premier integrated interdisciplinary technological institute spread in an area 25 acres , accommodating well ventilated classrooms, state-of-the art labs, well maintained outdoor & indoor sports and games facilities.

SVPP is affiliated to Jawaharlal Nehru Technological University Anaparamu (JNTUA); ISO 9001-2000 certified, approved by the All India Council for Technical Education (AICTE), New Delhi; and Accredited by NAAC with 'A' grade. The departments of Computer Science and Engineering (CSE), Electronics and Communication Engineering (ECE), Electrical and Electronics Engineering (EEE), are all accredited by the National Board of accreditation (NBA), New Delhi during 2012-13. SVPP Offers 5 Under graduate courses with a total intake of 540 seats, in addition it also offers 7 Post graduate Engineering courses with an intake of 18 in specialization. Besides it offers 2 PG courses namely MBA with an intake of 120 seats and MCA with an intake of 60. The campus is enabled with WiFi. Central library equipped with Audio-Visual Theatre, Digital Library, Good number of text books and journals of national and international repute. A fleet of 20 luxurious buses connecting the college from various places in the district.

70% of the Faculty on payroll was ratified by the affiliating University. Hence, SVPP recruits only distinguished academicians, research scientists and experienced faculty who have been carefully chosen through a search from a pool reputed organizations.

Our computing and engineering laboratories and workshops are equipped to meet all commercial and industrial standards and these facilities are available both in and out of class time for the students. Research centers use latest software such as AutoCAD, CATIA, Pro/E, MATLAB, VHDL, IBM Rational Rose, and so on. Students not only benefit from excellent teaching and research facilities, but also from strong links with the industry. Collaborative MOU's with companies such as Infosys, Cyient, Wipro, Zenopsys & ERDL not only help us remain at the forefront of technological advances, but also ensure that we keep pace with employers' changing expectations.

Our focus on employment runs through everything that we do and we will actively support you in your search for internships during your study.

SVPP also enjoys geographical advantage, as it is well connected by air, rail and road. The Campus is situated on Chennai – Bangalore Highway and is 20 Minutes drive from Tirupati Airport. In a lush green and pollution free environment.

PRINCIPAL'S DESK

Greetings from Dr.N.Sudhakar Reddy,
Dr.N.Sudhakar Reddy, as Principal of SVP CET, has always been a very proactive person and has involved himself in all the activities of the college. He displayed a keen interest in the affairs of the college and being a student-friendly person is avidly interested in the academic progress of the students and staff.

He completed his Ph.D at VEL TECH ,Chennai. He took up academic pursuit and has more than 14 years of academic, research and administrative experience. He served in an array of designations like Asst. Professor, Assoc. Professor, Professor and HOD at GATES Institute of Technology, Gooty, Sri Venkateswara College of Engineering, Chittoor, Sir Vishveshwariah institute of science and Technology, Madanapalli, Global College of Engineering & Technology, Kadapa, Madina Engineering College, Kadapa. Above all, he took up a mission to ignite the young minds so as to nurture the students with the potential to reach greater horizons. He strongly believed in himself, his mentors, peers and team.

He holds memberships in Indian Society for Technical Education (ISTE) and IAENG. He published more than 35 research papers in several national and international journals and conferences.

Thank you.

Best Wishes!
Principal
Dr.N.Sudhakar Reddy



Dr.N.Sudhakar Reddy
Principal

COLLEGE VISION

- ✦ Sri Venkatesa Perumal College of Engineering & Technology is dedicated for carving the youth as dynamic, competent, valued and knowledgeable professionals who shall lead the Nation to a better future.

DEPARTMENT VISION

- ✦ To become a center of excellence in Electronics and Communication Engineering and provide necessary skills to the students to meet the challenges in industry and society.

COLLEGE MISSION

- ✦ Providing Quality Education, student – centered teaching – learning processes and state-of-art infrastructure for professional aspirants hailing from both rural and urban areas.
- ✦ Imparting technical education that encourages independent thinking, develops strong domain of knowledge, hones contemporary skills and positive attitudes towards holistic growth of young minds.
- ✦ Evolving the Institution into a Center of Academic and Research Excellence

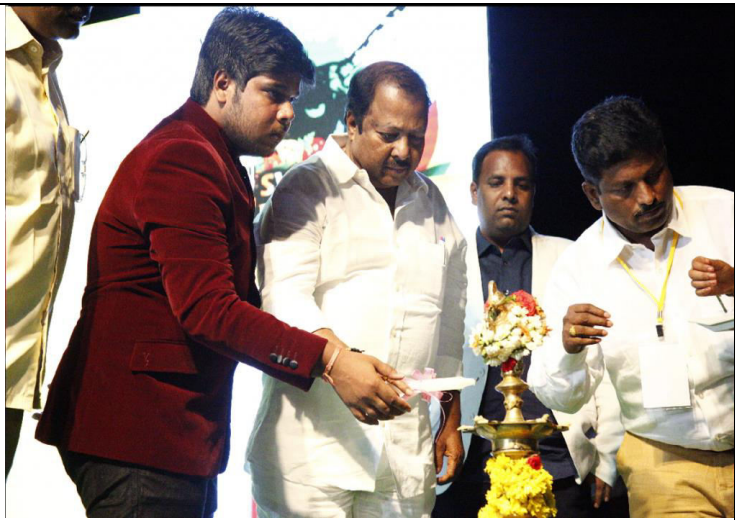
DEPARTMENT MISSION

- ✦ M1: Provide congenial academic ambience with necessary infrastructure and learning resources.
- ✦ M2: Inculcate confidence to face and experience new challenges from industry and society.
- ✦ M3: Ignite the students to acquire self-reliance in the latest technologies.
- ✦ M4: Foster Enterprising spirit among students.

Events

Vinayaka chavathi celebrations

The vinayaka chavathi was celebrated on 25th August 2017. Mr. Ravuri Balaji, Vice Chairman was the Chief Guest and Dr. Ravuri Venkataswamy Chairman was the Guest of Honour. Chairperson & Principal distributed awards to the students for their outstanding performances besides Staffs and Students were awarded for their achievements in Sports.



Annual day(SVP CET FIESTA 2K19) (12-04-2019)

INDEPENDENCE DAY CELEBRATIONS

On 15th August 2019 Independence Day celebration was started at 8.00 A.M. in the campus. All B.Tech/M.Tech/MBA & MCA students, NSS Volunteers about 450 members, All Teaching and Non Teaching staff of the institute have attended for the celebrations. All were assembled in C-Block Open Auditorium at 7.50 am. Our Vice Chairman was presided the event, Principal Dr. N.Sudhakar Reddy, and all the Heads of various Departments, NSS Program Officer, Administrative Officer, and Dean of Training and Placement section were gathered on the Dias.

The function was started with prayer song and Physical Director invited Vice Chairman to hoist the flag. Some of the students and staff sung the prayer song, flag song and patriotic songs.

Speeches:

Vice Chariman, Ravuri V Balaji spoke about corruption, education to rural people and measures to be taken to become developed country. He also suggested to all the students to give respect to the freedom fighters and to have minimum responsibility towards the development of the country. Principal Dr. N.Sudhakar Reddy spoke about Plastic Pollution and roles of citizens to make plastic free Puttur, which helps to protect human life, animal and plants. He also suggested several methods to avoid usage of plastic. Vice Principal emphasized on pollution free environment and created awareness on responsibilities of students. Various staff and students shared their view on Independence day.

Sweets Distribution:



At the end of celebration, sweets are distributed to all the students and staff members. Buses leave the campus at 11 am. The celebration made us to realize the primary responsibilities. Finally it was a very grand success.




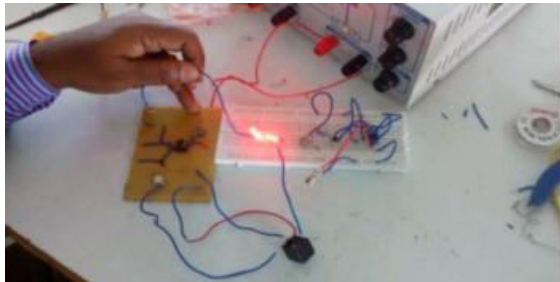
Workshops/Conferences/Symposiums/Guest lectures

2 days work shop on basic electron components and circuits



Title of Training Program	A 2- Day Hands-on Workshop on Basic Electronic Components and Circuits
Dates	10/8/2016 to 11/8/2016
Resource Persons	
Internal Trainer	Mrs.D.S.Vanaja
External Trainer	-
Convener	Mr. Sudhakar Reddy
Topics Covered	Basic components of Electronics (Identification of components RLC and semiconductors, symbols identification) and done projects on breadboard and PCB

Workshop Summary	Workshop on Basic components of Electronics mainly focuses on Identification of basic electronic components (Resistors, inductors, capacitors, diodes, transistors etc...). Identification of symbols in basic electronic circuits. Testing of components i.e., RLC and semiconductor devices in various faults(Short, open, leaky, and good one).Tracing analysis of integrated circuit board and also worked on Mini projects.
Sample Photos	
Title of Training Program	A 2- Day Hands-on Workshop on Basic Electronic Components and Circuits–(For BTECH II YEAR)
Dates	19 th to 22 th July 2017
Resource Persons	
Internal Trainer	Mr. C. Manikanta, Asst. Professor. Mr.K.D.Mohana sundaram, Asst.Professor.
Convener	Mr. N. Sudhakar Reddy, Assoc. Professor
Topics Covered	Basic components of Electronics (Identification of components RLC and semiconductors, symbols identification) and done projects on breadboard and PCB
Workshop Summary	A 2- Day Hands-on Workshop on Basic Electronic Components and Circuits–(For BTECH II YEAR) was conducted on 19 th to 22 nd July 2107 . the resource persons Mr. C. Manikanta, Asst. Professor. Mr.K.D.Mohana sundaram, Asst.Professor shared their knowledge with the students . Workshop on Basic components of Electronics mainly focuses on Identification of basic electronic components (Resistors, inductors, capacitors, diodes, transistors etc...). Identification of symbols in basic electronic circuits. Testing of components i.e., RLC and semiconductor devices in various faults (Short, open, leaky, and good one).Tracing analysis of integrated circuit board and also worked on Mini projects.
Sample Photos	

Two days work shop on PCB design 2016-2017

Title of the Training program	Two day workshop on PCB Design
Dates	15/09/2016 to 16/09/2016
Resource Persons	
Internal Trainers	Mr.L.Purusotham,Crest Technology.
convener	Mr.G.Sunil
Topics covered	
Workshop Summary	<p>The Workshop is dedicated in training the students on the design and manufacturing of a Printed Circuit Board. This workshop is an invaluable resource for those who are learning PCB design. Designing a PCB is not something you will do in a couple of hours. It is a highly technical learned skill that will take years to master. The Workshop will provide the basics of PCB design as well as more advanced topics. This is a two day workshop which focused on advanced topic of PCB design along Fabrication process. Each participant will involve in etching process on a laminated copper sheet provided to them, they will also under go for drilling process using a Hand driller and finally assembling and soldering the electrical components onto board.</p>
Sample photos	 

3 days work shop on VLSI design

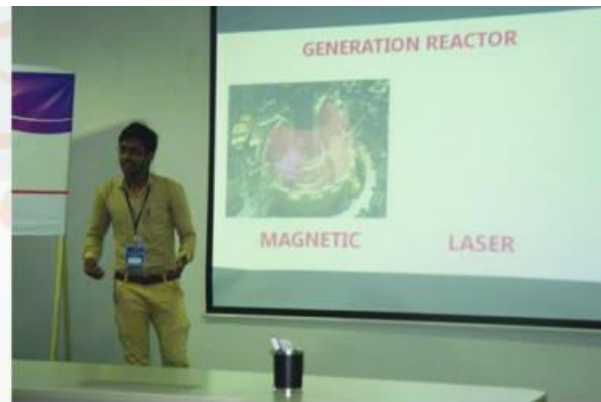
Title of the Training program	3 day Workshop ON VLSI PHYSICAL DESIGN BY ERDL
Dates	20/10/2016 TO 21/10/2016
Resource Persons	Dr.Durga Prasad ERDL
Convenor	Mr. K.Mohona Sundaram
Faculty Coordinators	Mr.C.Manikanta
Topics covered	VLSI physical Design
cription about the Event	The course will introduce the participants to the basic design flow in VLSI physical design automation, the basic data structures and algorithms used for implementing the same. The course will also provide examples and assignments to help the participants to understand the concepts involved, and appreciate the main challenges therein.
Sample photos	 



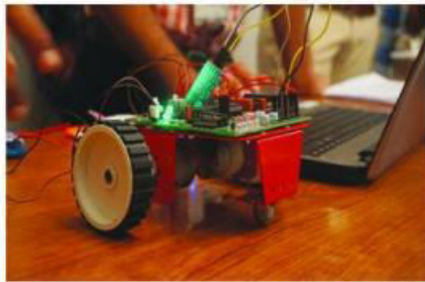
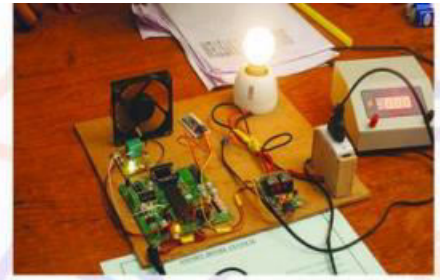
❖ **Basics of software designing at Centre 2 with Proteous Software:**



TECH WIZZ 2K17



PROJECT WORKS



Placements

Name of the Industry / Organization	Date of the MoU	Specific Roles and Functions	Related POs & PSOs
Microsoft IT Academy	31 st July, 2015 to 30 th July, 2017	Training	PO1, PO2, PSO1, PSO2
ERDL (E Research Design Labs)	16 th Sept 2016 to 15 th Sept, 2020	Internship, Training & Placement	PO1, PO2, PO5, PSO1, PSO2
Zenopsys Technologies	23 rd Dec, 2016 to 22 nd Dec, 2019	Incubation center, Internship, Training & Placement	PO1, PO2, PO3, PO5, PSO1, PSO2
Cyient	3 rd Nov, 2016 to 2 nd Nov, 2019	Pool Campus Drives	PO1, PO2, PO9, PO10, PSO1, PSO2
YIIT	22 nd Oct, 2015 to 21 st Oct, 2020	On job Training, Technical sharing, Faculty Exchange, Research Consultancy, Placements & Training	PO1, PO2, PSO1, PSO2



SRI VENKATESA PERUMAL COLLEGE OF ENGINEERING & TECHNOLOGY
(AUTONOMOUS)

PUTTUR

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